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11/21/01*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#13/A

Application of

Applicants : John D. Porter and William N. Thompson
Serial No. :
Filed :
Title : LATCH-UP PREVENTION FOR MEMORY CELLS
Docket No. : MIO 0042 V2

Assistant Commissioner for Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on December 18, 2000.

Gregory J. Adams 44,494
Gregory J. Adams Reg. No.

Sir:

PRELIMINARY AMENDMENT

Examination and favorable consideration are respectfully requested.

*1300
10-5-01*

IN THE SPECIFICATION

At page 1 after the Title, please insert the following:

--CROSS REFERENCE TO RELATED APPLICATIONS

This application is a divisional of U.S. Patent Application Serial No. 09/368,710, filed August 5, 1999.--

IN THE CLAIMS

Please cancel claims 1-6 in the application.

Please add the following claims:

A method of fabricating a memory device comprising:
forming a first semiconductor structure;

*Not enter
due to #418 as
MIT Disregard.*

4/28

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